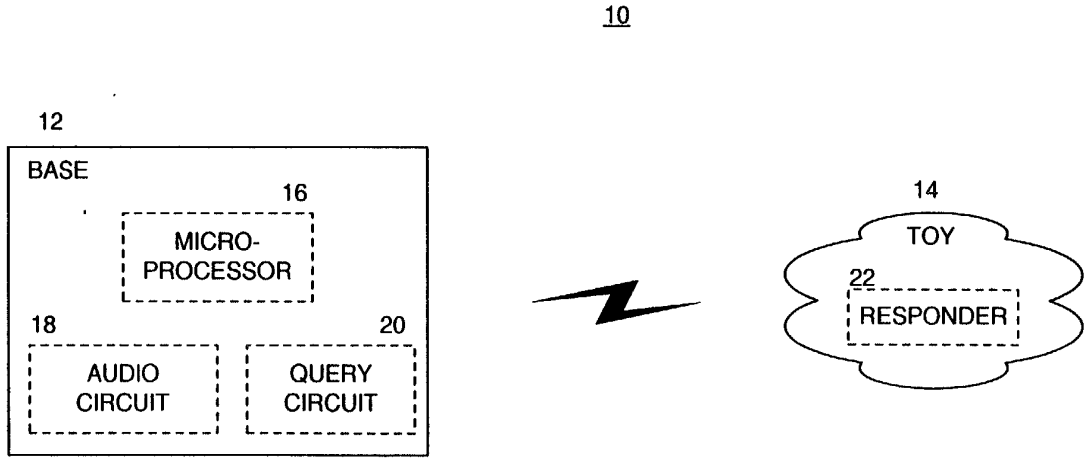

FIG. 1
(Prior Art)

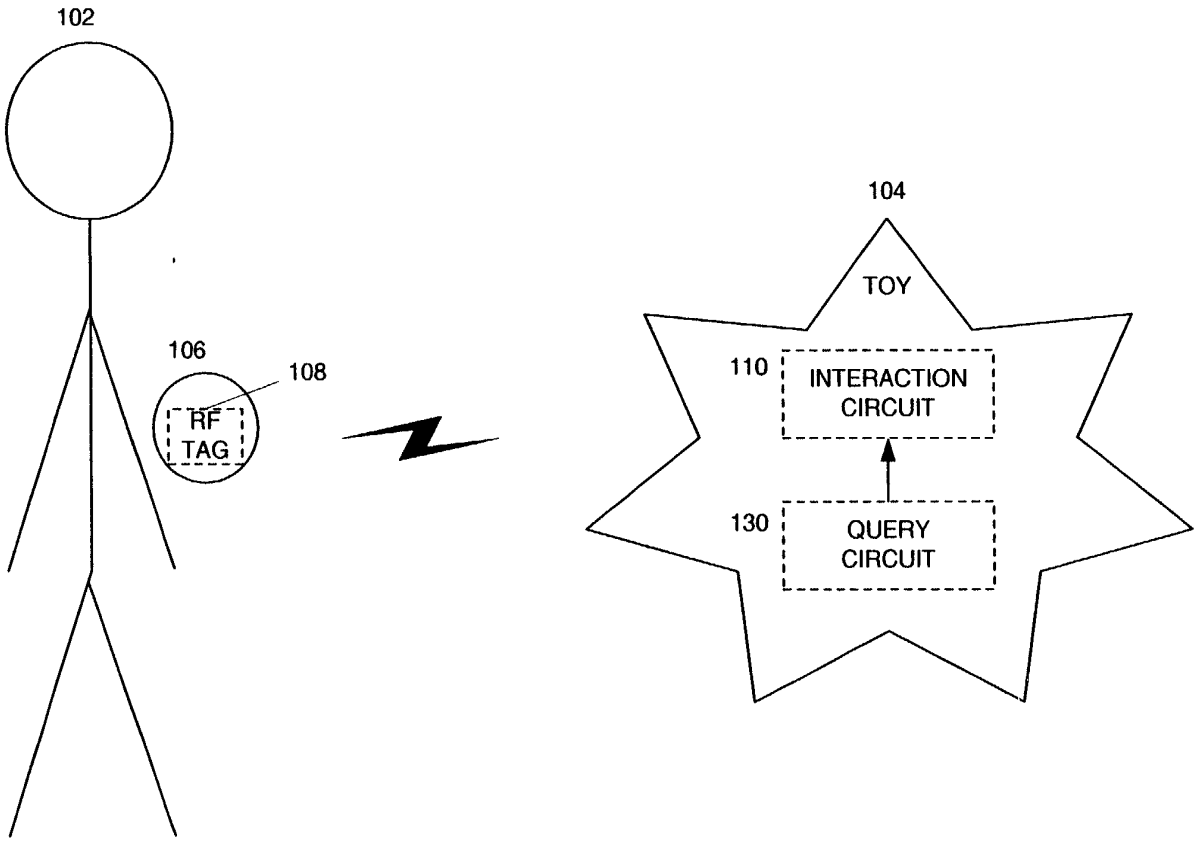

FIG. 2

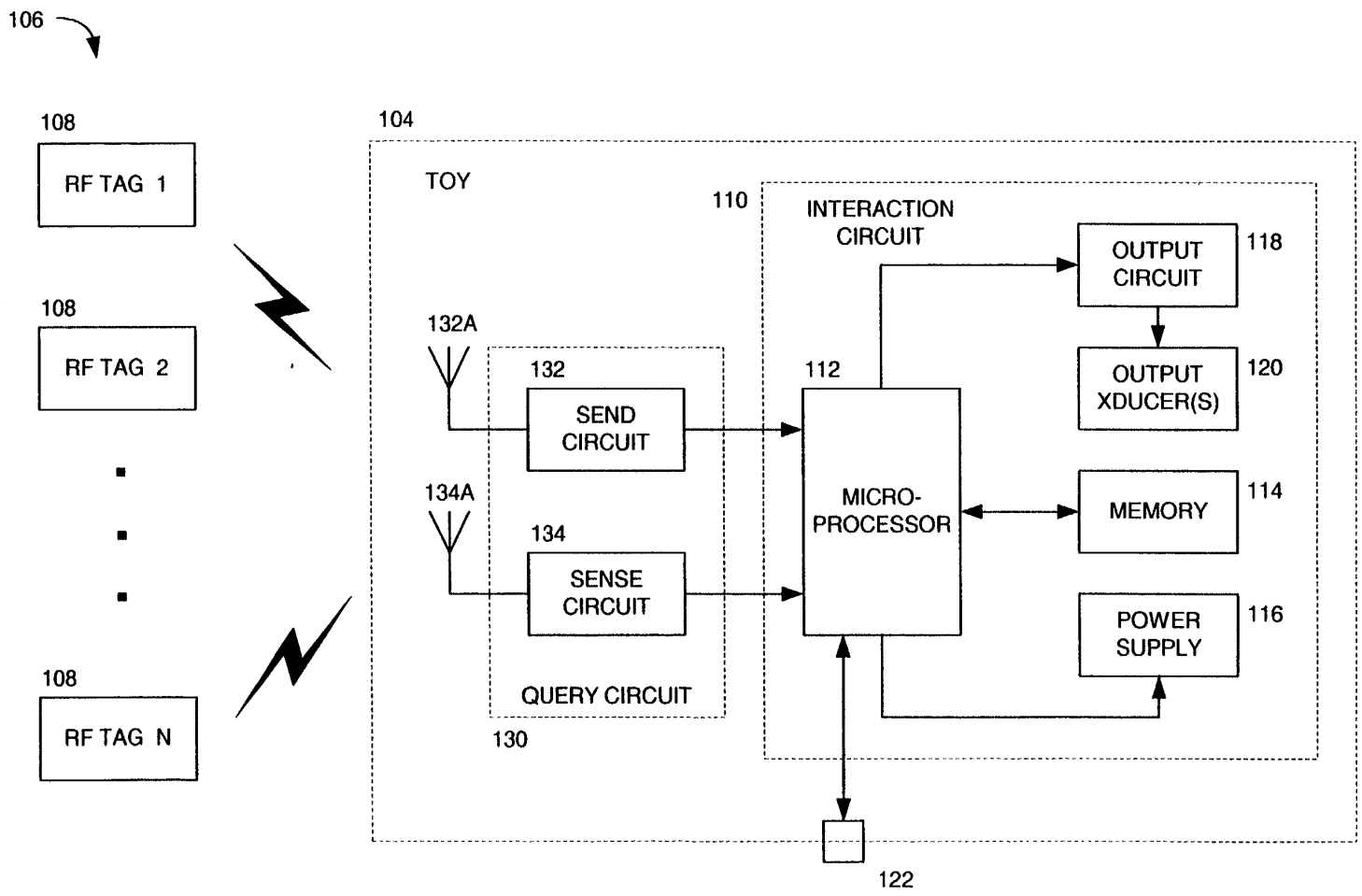
106
108
RF TAG 1
108
RF TAG 2
108
RF TAG N
104
TOY
110
INTERACTION CIRCUIT
OUTPUT CIRCUIT
118
OUTPUT XDUCER(S)
120
132A
132
SEND CIRCUIT
112
MICRO-PROCESSOR
MEMORY
114
134A
134
SENSE CIRCUIT
POWER SUPPLY
116
QUERY CIRCUIT
130
122

FIG. 3

200

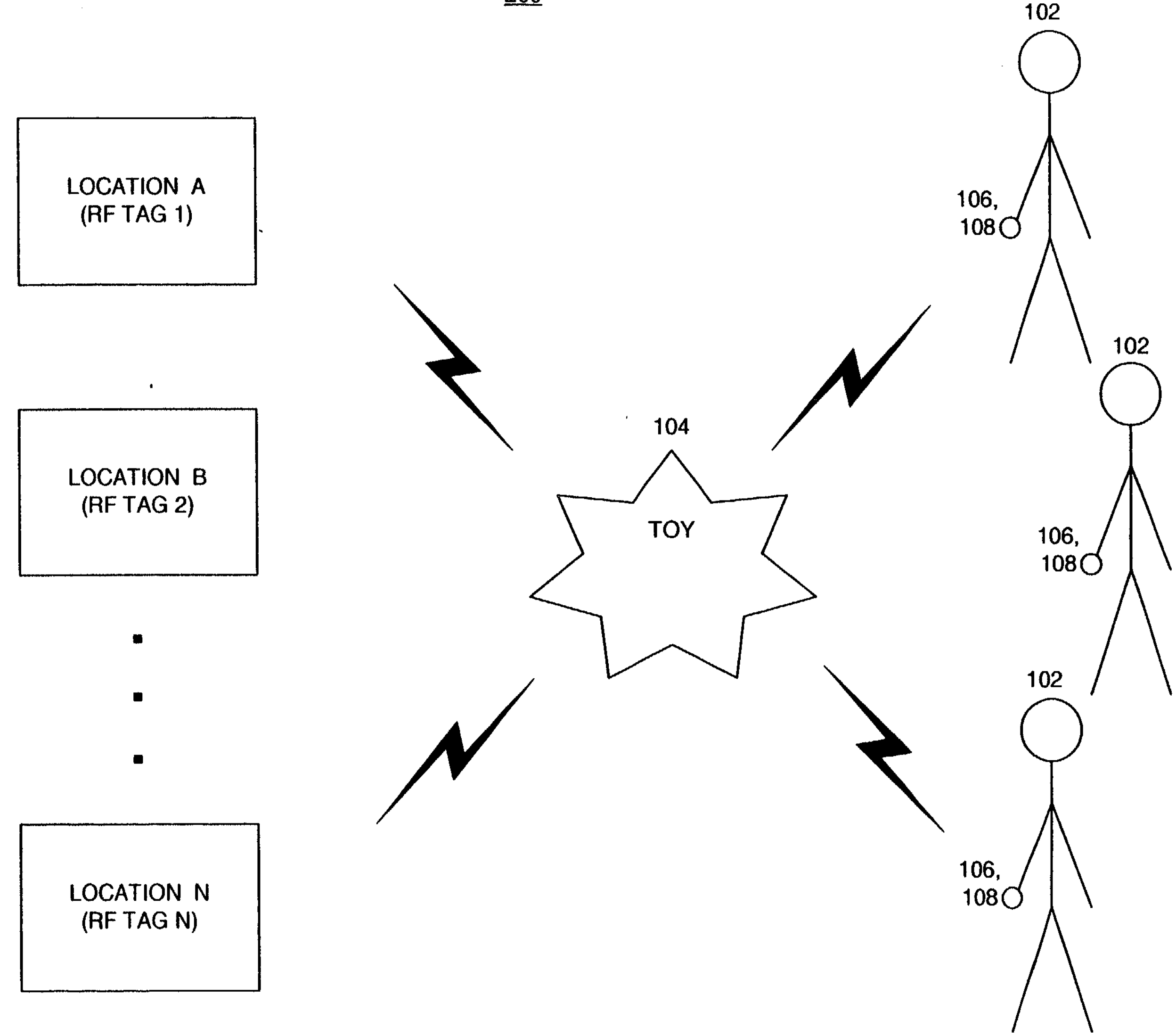
LOCATION A
(RF TAG 1)
LOCATION B
(RF TAG 2)
LOCATION N
(RF TAG N)
104
TOY
102
106,
108
102
106,
108
102
106,
108

FIG. 4